



**DEFENSE LOGISTICS AGENCY**  
DEFENSE SUPPLY CENTER, COLUMBUS  
POST OFFICE BOX 3990  
COLUMBUS, OH 43218-3990

IN REPLY  
REFER TO

DSCC-VQ (VQC-08-015415/Mr. Granchi/614-692-9975/dg)

SUBJECT: Commercial Laboratory Suitability Status, MIL-STD-883, FSC 5962

Mr. Thomas S. Speakman  
Managing Director  
Silicon Cert Ltd.  
4201 Pottsville Pike  
Reading, PA 19605

Dear Mr. Speakman

Silicon Cert Ltd. has demonstrated to the Defense Supply Center, Columbus (DSCC) that the Particle Impact Noise Detection (PIND) systems are in compliance with MIL-PRF-38535 and MIL-STD-883 Method 2020. Laboratory Suitability for MIL-STD-883 Method 2020 PIND is effective 6 March 2008. A full list of test methods for which Silicon Cert Ltd. is considered suitable to perform is listed on the enclosure to this letter.

The previous commercial laboratory suitability letter (VQC-07-013663) granted to Silicon Cert, Ltd. by DSCC is superseded by this letter.

Your laboratory is to maintain a record for all microcircuit testing and submit a summary annually to DSCC-VQC which will include the following as a minimum:

- a. Military part number, SMD, or MIL-STD-883 identification
- b. Date code
- c. Quantity tested
- d. Number of devices pass/fail
- e. Manufacturer
- f. Manufacturer's lot number
- g. Test method(s)/condition(s)
- h. Date test completed
- i. Qualification test report number as applicable
- j. Self audit reports with deficiencies and corrective actions

Your present reporting period is from 1 July 2007 through 30 June 2008 and is due by 31 July 2008.

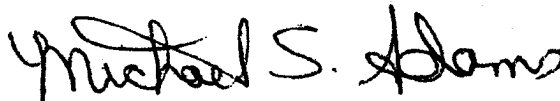
Test labs shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their military products utilizing the test methods on the attached enclosure. Failure to provide prior notification may be grounds for removal from DSCC's Commercial Lab Suitability Listing.



This laboratory suitability is valid subject to the conditions as stated in DoD 4120.24-M and SD-6. All processing procedures will incorporate the handling, testing, and packaging requirements according to the guidelines in JEDEC Publication 108 and EIA-STD-5. This laboratory suitability is valid until withdrawn by DSCC-VQC.

If you have any questions, please contact Mr. Granchi at (614) 692-9975.

Sincerely,

A handwritten signature in black ink that reads "Michael S. Adams". The signature is written in a cursive style with a large initial "M".

MICHAEL S. ADAMS  
Chief  
Custom Devices Team

Enclosure

cc:

VQC (Michael Grammens)

VQC (Scott Thomas)

VQ (Alberta Petruskevich)

Silicon Cert Ltd. (John Schmoyer)

Visit us on the web at: [http://www.dscclia.mil/offices/sourcing\\_and\\_qualification/](http://www.dscclia.mil/offices/sourcing_and_qualification/)

Enclosure to DSCC-VQ (VQC-08-015415)

TEST	METHOD/CONDITION
Stabilization Bake	1008/A-D
Temperature Cycling	1010/A-D, F
Thermal Shock	1011/A-C
Seal	1014/A1, A2, C3
Constant Acceleration	2001/A-F
Mechanical Shock	2002/A-B
Solderability	2003/Test A-C and Category 2-3 (J-STD-002)
Variable Frequency Vibration	2007/A
Resistance to Solvents	2015
PIND	2020/A,B
Random Vibration	2026/I-II